

REMARKS

Claims 27 to 48 are newly pending in the present application, of which claims 27 and 37 to 41 are independent. Claims 1-26 have been cancelled. Support for the newly submitted claims can be found in the description of the embodiments shown, for example, in figures 6, 7, 8 and 9, and described, for example, in paragraphs 1053, 1055, 1059, 1062, 1064, 1066 and 1068 to 1072. Reconsideration and further examination is respectfully requested.

Claims 1 to 4, 11, 13 to 17, 20 to 22 and 26 were rejected under 35 U.S.C. § 103(a) over U.S. Patent Publication No. 2002/0136278 (Nakamura). Claims 5, 6 and 8 were rejected under 35 U.S.C. § 103(a) over Nakamura in view of U.S. Patent Publication No. 2004/0017843 (Fitton). Claims 7, 12 and 18 were rejected under 35 U.S.C. § 103(a) over Nakamura in view of Fitton and U.S. Patent Publication No. 2003/0067967 (Miyoshi). Claims 9 and 10 were rejected under 35 U.S.C. § 103(a) over Nakamura in view of U.S. Patent Publication No. 2005/0277419 (Takano). Claim 19 was rejected under 35 U.S.C. § 103(a) over Nakamura in view of U.S. Patent No. 6,628,631 (Mazawa). Claim 23 was rejected under 35 U.S.C. § 103(a) over Nakamura in view of U.S. Patent Publication No. 2004/0071199 (Boesel). Claims 24 and 25 were rejected under 35 U.S.C. § 103(a) over Nakamura in view of Boesel and U.S. Patent No. 6,295,023 (Bloebaum). Claims 1 to 26 have been cancelled without prejudice or disclaimer of subject matter, and without conceding the correctness of the rejections. In view of this cancellation, the rejections are considered moot.

Newly presented independent claim 27 recites an integrated circuit including a despreading unit operative to despread input samples and provide despread symbols with a first spreading factor, a channel compensation unit operative to multiply the despread symbols with channel estimates and provide demodulated symbols, a symbol combiner operative to combine groups of demodulated symbols by combining at least one accumulated demodulated symbol with a current demodulated symbol to obtain recovered data symbols for a channel with a second spreading factor that is an integer multiple of the first spreading factor.

Independent claims 37 to 41 all correspond generally to independent claim 27.

The previously-cited art is not seen to teach or suggest the features of independent claims 27 and 37 to 41, and in particular, are not seen to teach or suggest at least the features of a despreading unit operative to despread input samples and provide despread symbols with

a first spreading factor, and a symbol combiner operative to combine groups of demodulated symbols by combining at least one accumulated demodulated symbol with a current demodulated symbol to obtain recovered data symbols for a channel with a second spreading factor that is an integer multiple of the first spreading factor.

As such, the application is believed to be in condition for allowance and such action is respectfully requested.

CONCLUSION

In light of the amendments contained herein, Applicant submits that the application is in condition for allowance, for which early action is requested.

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Respectfully submitted,

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